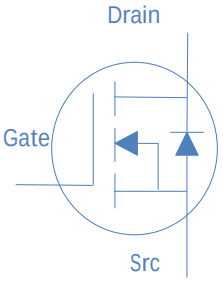
120V N-Ch Power MOSFET

Feature

- High Speed Power Switching, Logic Level
- Enhanced Body diode dv/dt capability
- Enhanced Avalanche Ruggedness
- 100% UIS Tested, 100% Rg Tested
- Lead Free,Tf8 67 528 8 reW*nBT/F41216 Tf1 0 0 1 619 65333 Tm0 g9 Tf1 0 0 1 619 65333 Tm0 g0 G

V_{DS}		120	V
$R_{DS(on),typ}$	V_{GS}=10V	9.8	m
$R_{DS(on),typ}$	V_{GS}=4.5V	12.0	m
$I_{D\ (Sillicon\ Limited)}$		68	A



Part Number	Package	Marking
HGI130N12SL	TO-251	GI130N12SL
HGD130N12SL	TO-252	GD130N12SL

Absolute Maximum Ratings at T_j

Parameter	Symbol	Conditions	Value	Unit
Continuous Drain Current (Silicon Limited)	I_D	T_C	68	A
		T_C	48	
Drain to Source Voltage	V_{DS}	-	120	V
Gate to Source Voltage	V_{GS}	-	20	V
Pulsed Drain Current	I_{DM}	-	260	A
Avalanche Energy, Single Pulse	E_{AS}	L=0.4mH, T_C	320	mJ
Power Dissipation	P_D	T_C	136	W
Operating and Storage Temperature	T_J, T_{stg}	-	-55 to175	

Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-Ambient	R_{JA}	50	
Thermal Resistance Junction-Case	R_{JC}	1.1	

Electrical Characteristics at T_j

Static Characteristics

Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=250 A	120	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS}=V_{DS}, I_D=250 A	1.4	2.0	2.4	
Zero Gate Voltage Drain Current	I_{DSS}	V_{GS}=0V, V_{DS}=120V, T_j	-	-	1	A
		V_{GS}=0V, V_{DS}=120V, T_j	-	-	100	
Gate to Source Leakage Current	I_{GSS}	V_{GS} s99 $_{DS}$=0V	-	-	100	nA
Drain to Source on Resistance	$R_{DS(on)}$	V_{GS}=10V, I_D=20A	-	9.8	12.5	m
Drain to Source on Resistance	$R_{DS(on)}$	V_{GS}=4.5V, I_D=20A	-	12	17	m
Transconductance	g_{fs}	V_{DS}=5V, I_D=20A	-	65	-	S
Gate Resistance	R_G	V_{GS}=0V, V_{DS} Open, f=1MHz	-	2.2	-	

Dynamic Characteristics

Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=60V, f=1MHz	-	2056	-	pF
Output Capacitance	C_{oss}		-	222	-	
Reverse Transfer Capacitance	C_{rss}		-	7.9	-	
Total Gate Charge	Q_g(10V)	V_{DD}=60V, I_D=20A, V_{GS}=10V	-	31	-	nC
Total Gate Charge	Q_g(4.5V)		-	15	-	
Gate to Source Charge	Q_{gs}		-	8	-	
Gate to Drain (Miller) Charge	Q_{gd}		-	4	-	
Turn on Delay Time	$t_{d(on)}$	V_{DD}=60V, I_D=20A, V_{GS}=10V, R_G=10 ,	-	11	-	ns
Rise time	t_r		-	9	-	
Turn off Delay Time	$t_{d(off)}$		-	18	-	
Fall Time	t_f		-	10	-	

Reverse Diode Characteristics

Diode Forward Voltage	V_{SD}	V_{GS}=0V, I_F=20A	-	0.9	1.2	V
Reverse Recovery Time	t_{rr}	V_R=60V, I_F=20A, dI_F/dt=100A/ s	-	50	-	ns
Reverse Recovery Charge	Q_{rr}		-	75	-	nC

Fig 1. Typical Output Characteristics

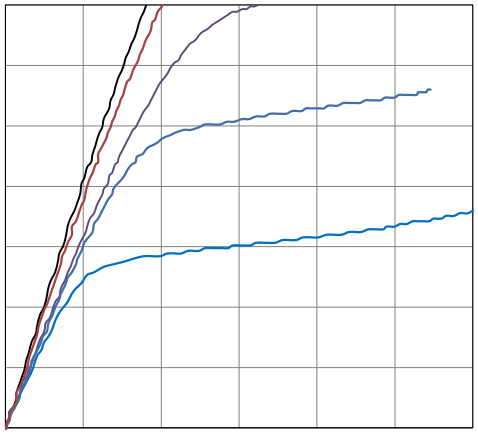


Figure 2. On-Resistance vs. Gate-Source Voltage

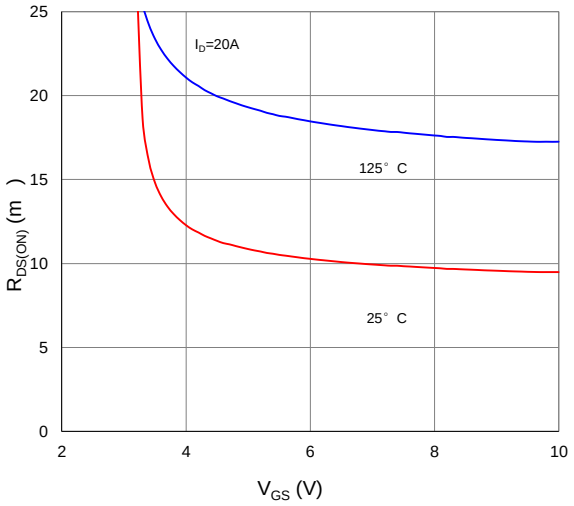


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

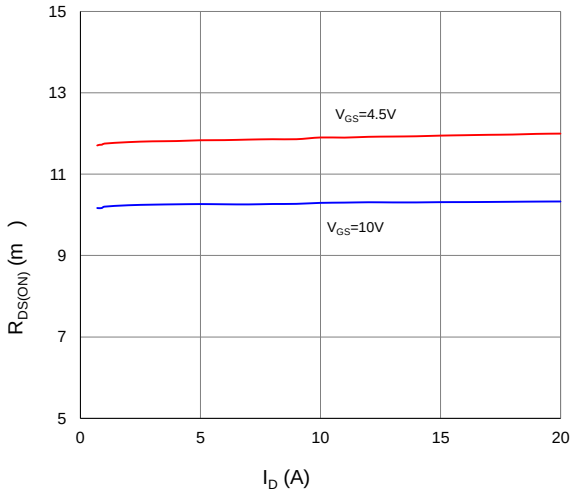


Figure 4. Normalized On-Resistance vs. Junction Temperature

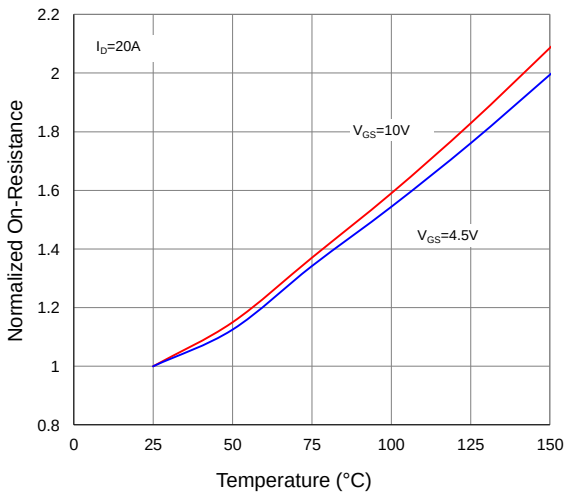


Figure 5. Typical Transfer Characteristics

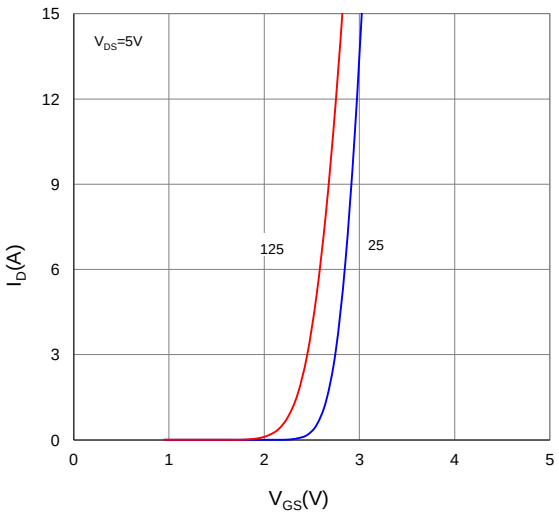


Figure 6. Typical Source-Drain Diode Forward Voltage

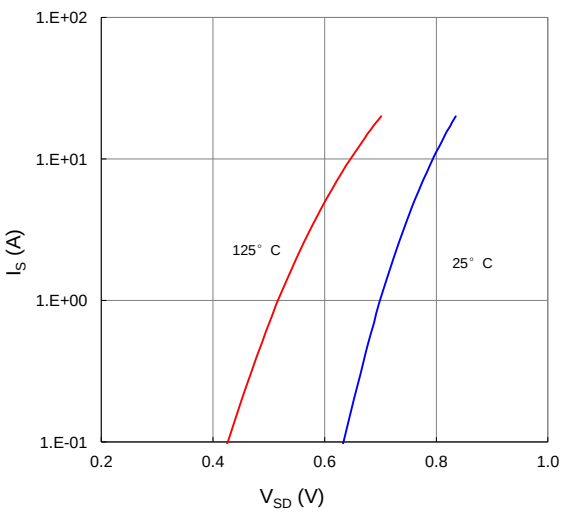


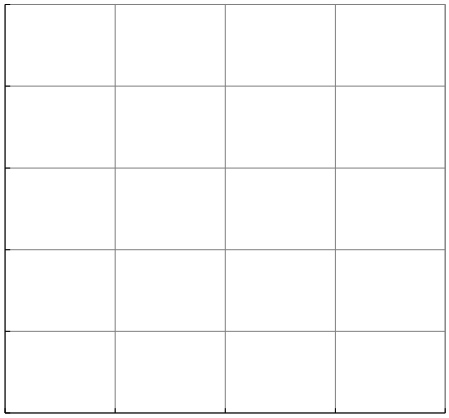
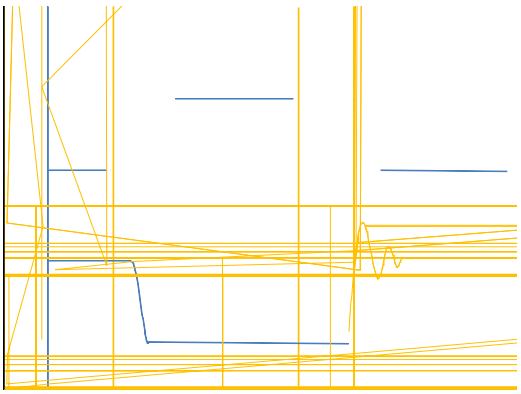
Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage	Figure 8. Typical Capacitance vs. Drain-to-Source Voltage
	

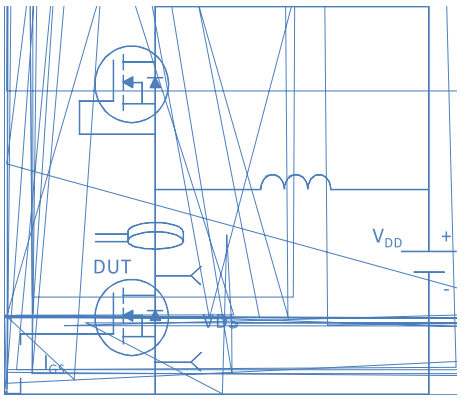
Figure 9. Maximum Safe Operating Area	Figure 10. Maximun Drain Current vs. Case Temperature

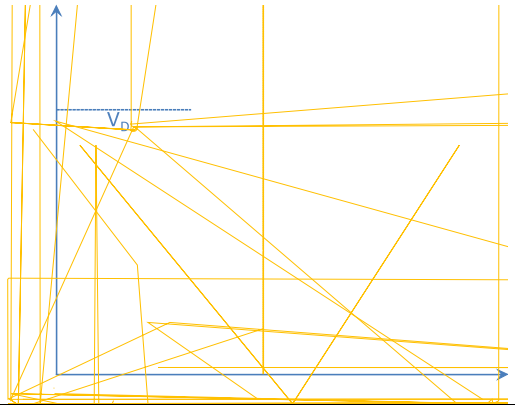
Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Ambient

Inductive switching Test

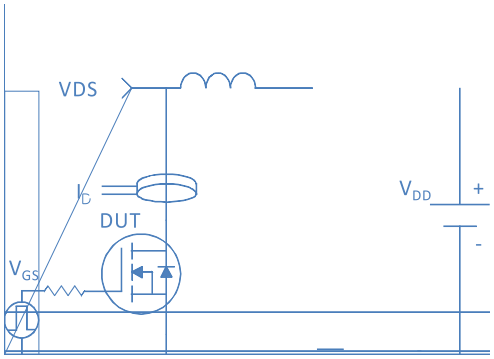


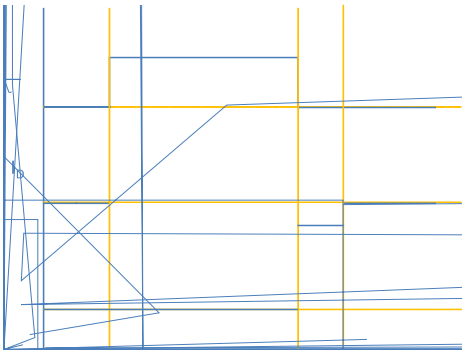
Gate Charge Test



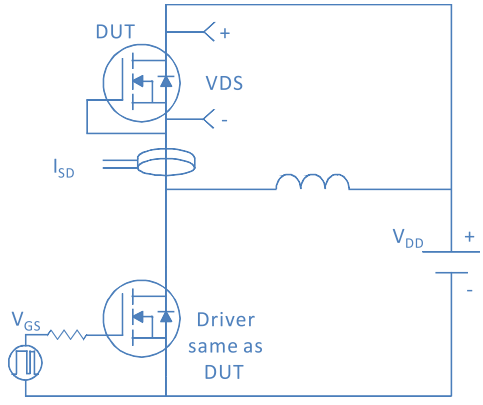


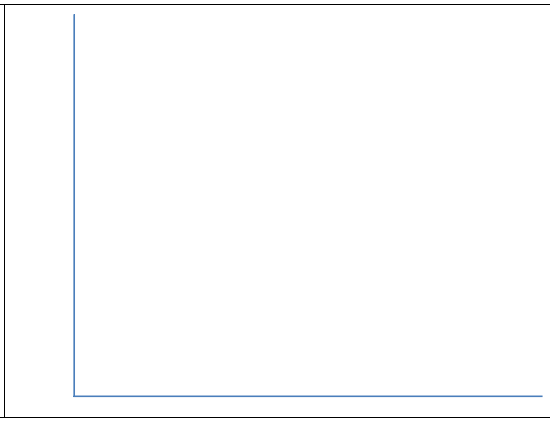
Uclamped Inductive Switching (UIS) Test





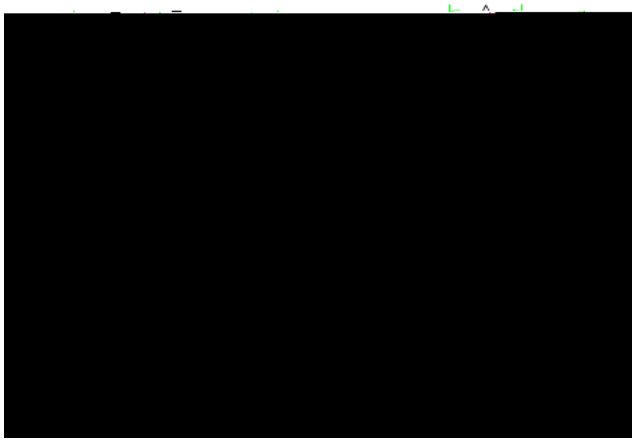
Diode Recovery Test



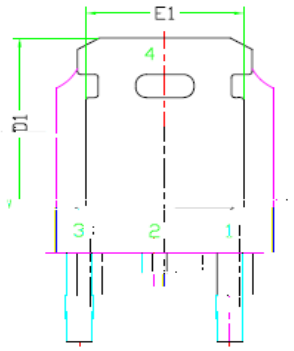


Package Outline

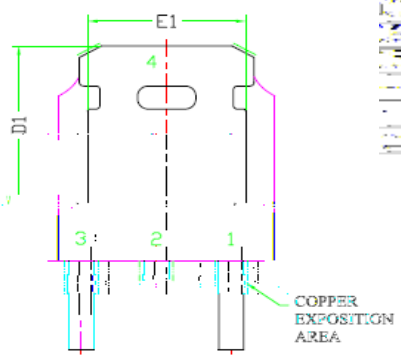
TO-252, 2 Leads



SYMBOL	DIMENSIONAL REQMTS		
	MIN	NOM	MAX
E	6.40	6.60	6.731
L	1.40	1.52	1.77
L1		2.743 REF	
L2		0.508 BSC	
L3	0.89	--	1.27
L4	0.64	--	1.01
L5	--	--	--
D	6.00	6.10	6.223



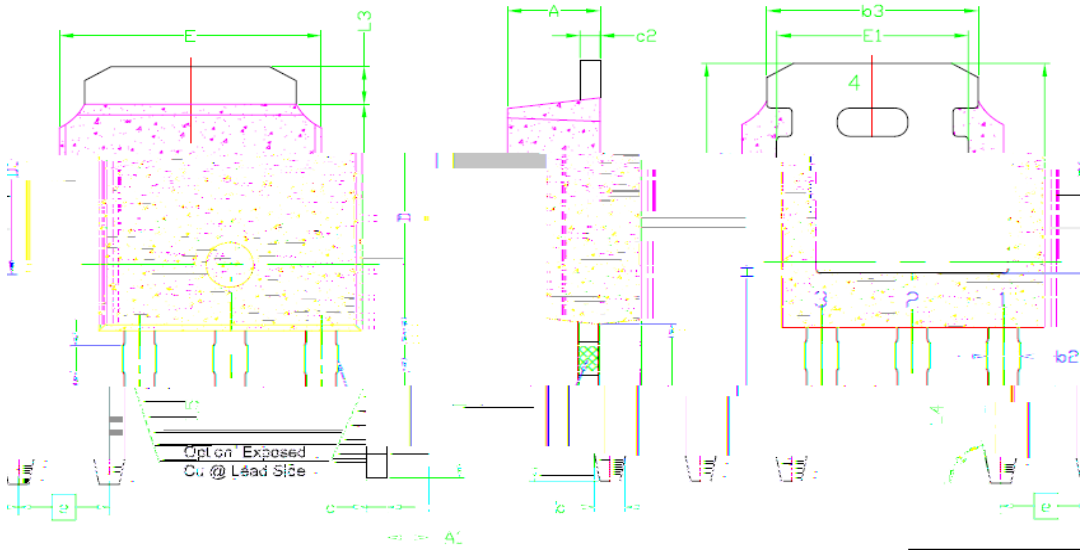
SINGLE ROW(NEW)



MATRIX L/F

Package Outline

TO-251, 3 leads



SYMBOL	DIMENSIONAL REQMTS		
	MIN	NOM	MAX
E	6.40	6.60	6.731
L	3.98	4.13	4.28
L3	0.89	--	1.27
L4		0.698 REF	
L5	0.972	1.099	1.226
D	6.00	6.10	6.223
H	11.05	11.25	11.45
b	0.64	0.76	0.88
b2	0.77	0.84	1.14
b3	5.21	5.34	5.46
e		2.286 BSC	
A	2.20	2.30	2.38
A1	0.89	1.04	1.15
c	0.46	0.50	0.60
c2	0.46	0.50	0.60
D1	5.10	--	--
E1	4.40	--	--
α		79° REF	